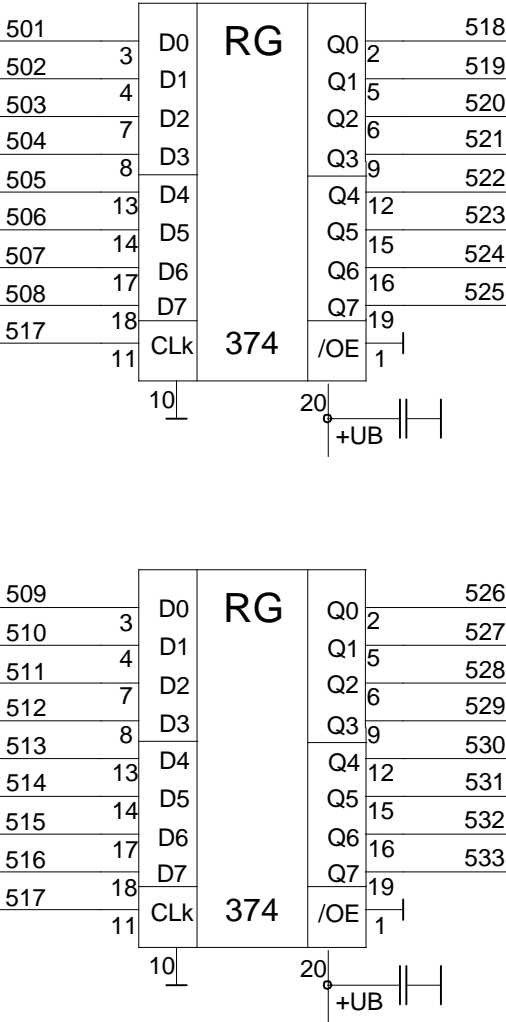


INK-Auswahl (->WR)

4x4Bitplan-Cache



# 2x8Bit-SR (D0/4-D7/11)

